

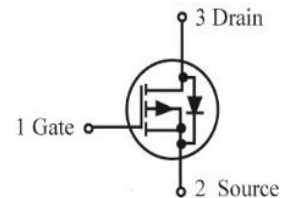
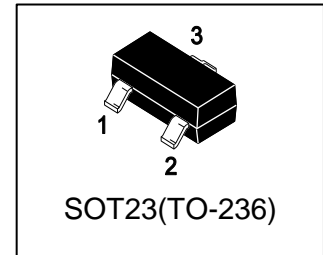
LP3401LT1G

S-LP3401LT1G

30V P-Channel Enhancement-Mode MOSFET

1. FEATURES

- $V_{DS} = -30V$
- $R_{DS(ON)} < 70m\Omega$ ($V_{GS} = -10V$)
- $R_{DS(ON)} < 80m\Omega$ ($V_{GS} = -4.5V$)
- $R_{DS(ON)} < 120m\Omega$ ($V_{GS} = -2.5V$)
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



2. APPLICATIONS

- Advanced trench process technology
- High density cell design for ultra low on-resistance.

3. DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
LP3401LT1G	A1	3000/Tape&Reel
LP3401LT3G	A1	10000/Tape&Reel

4. MAXIMUM RATINGS($T_a = 25^{\circ}C$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DSS}	-30	V
Gate-to-Source Voltage – Continuous	V_{GS}	± 12	V
Drain Current			A
– Continuous $T_A = 25^{\circ}C$ (Note 1)	I_D	-4.2	
– Pulsed (Note 2)	I_{DM}	-30	

5. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Power Dissipation(Note 1)	PD	1.4	W
Thermal Resistance, Junction-to-Ambient(Note 1)	$R_{\theta JA}$	125	$^{\circ}C/W$
Junction and Storage temperature	T_J, T_{stg}	$-55 \sim +150$	$^{\circ}C$

- 1.The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}C$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10s$ thermal resistance rating.
- 2.Repetitive rating, pulse width limited by junction temperature.

6. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

OFF CHARACTERISTICS

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain–Source Breakdown Voltage (VGS = 0, ID = -250 μ Adc)	VBRDSS	-30	-	-	Vdc
Zero Gate Voltage Drain Current (VGS = 0, VDS = -24 Vdc)	IDSS	-	-	-1	μ Adc
Gate–Body Leakage Current, Forward (VGS = 12 Vdc)	IGSSF	-	-	100	nAdc
Gate–Body Leakage Current, Reverse (VGS = -12 Vdc)	IGSSR	-	-	-100	nAdc

ON CHARACTERISTICS (Note 3)

Gate Threshold Voltage (VGS = -4.5Vdc, VDS = -5Vdc)	ID(on)	-25	-	-	Adc
Gate Threshold Voltage (VDS = VGS, ID = -250 μ Adc)	VGS(th)	-0.6	-1.0	-1.3	Vdc
Static Drain–Source On–State Resistance (VGS = -10 Vdc, ID = -4.2 Adc) (VGS = -4.5 Vdc, ID = -4 Adc) (VGS = -2.5 Vdc, ID = -1 Adc)	RDS(on)	-	-	70 80 120	m Ω

DYNAMIC CHARACTERISTICS

Input Capacitance (VGS = 0 V, f = 1.0MHz, VDS= -15 V)	Ciss	-	954	-	pF
Output Capacitance (VGS = 0 V, f = 1.0MHz, VDS= -15 V)	Coss	-	115	-	pF
Reverse Transfer Capacitance (VGS = 0 V, f = 1.0MHz, VDS= -15 V)	Crss	-	77	-	pF

SWITCHING CHARACTERISTICS

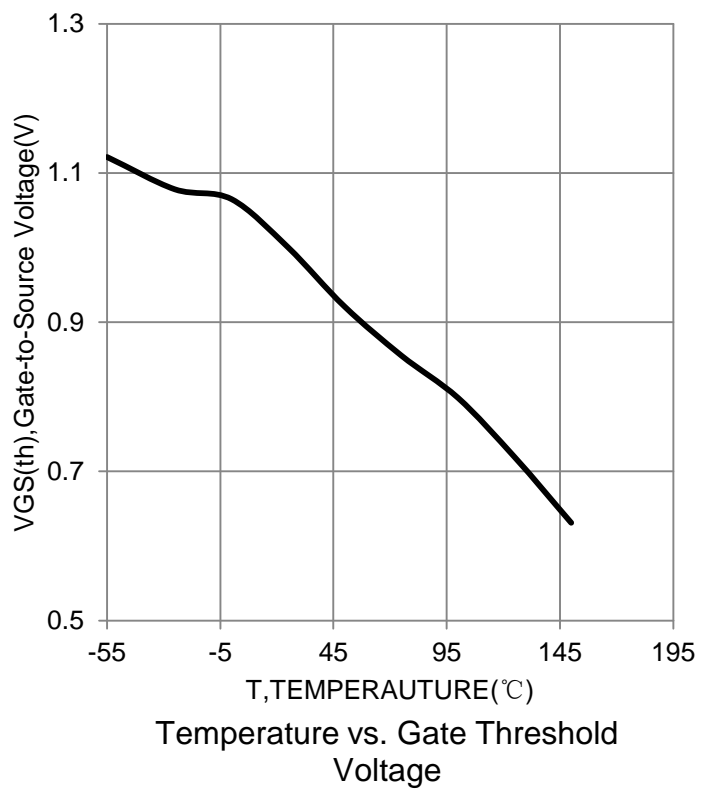
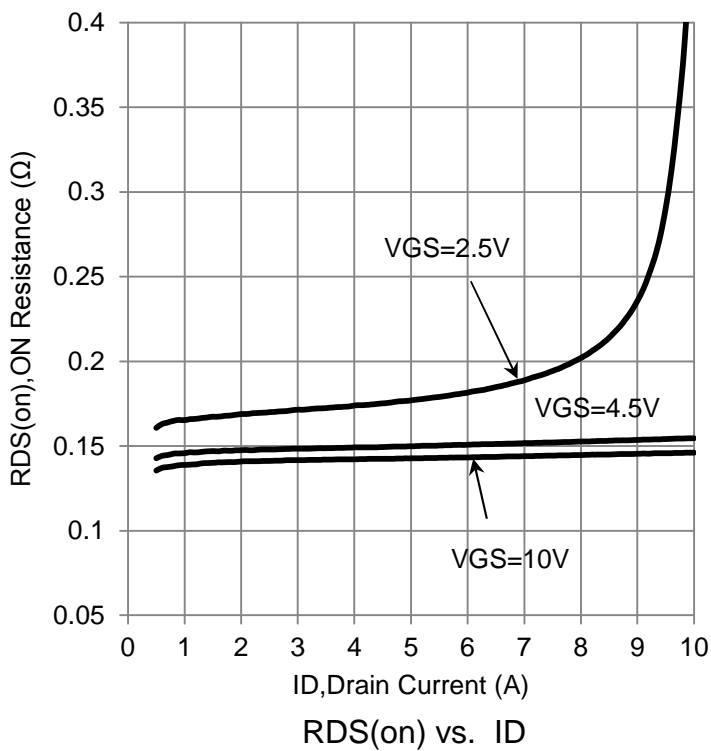
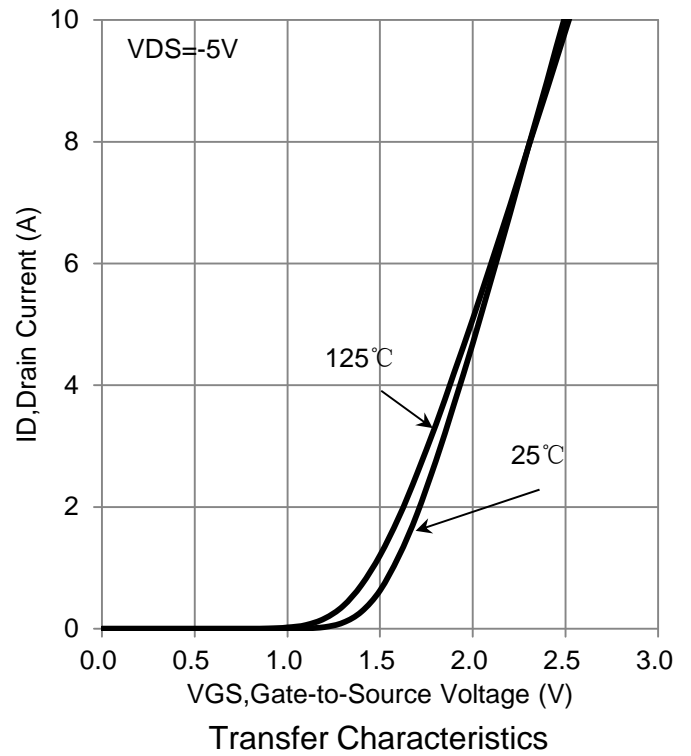
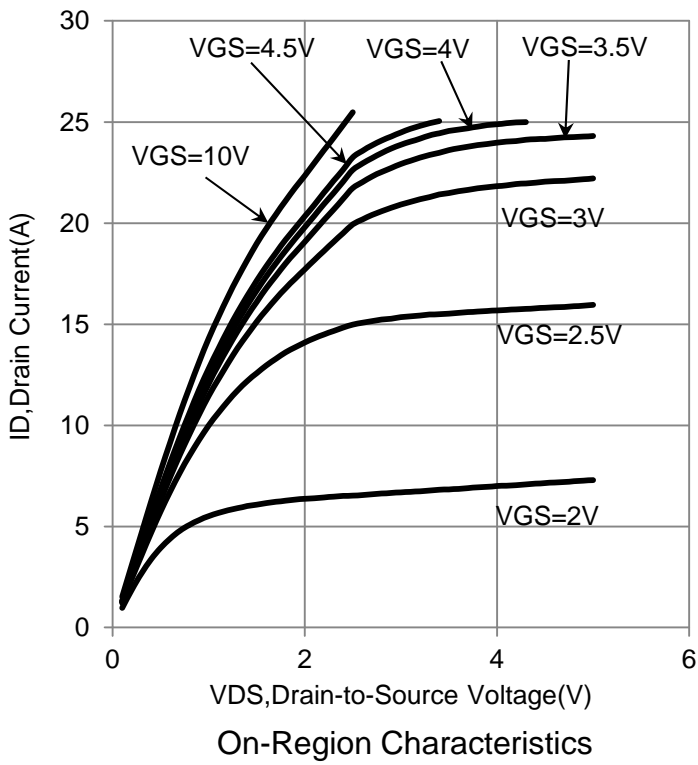
Turn-On Delay Time	(VDS=-15V, RL =3.6 Ω , RGEN=6 Ω ,VGS=-10V)	td(on)	-	6.3	-	ns
Rise Time		tr	-	3.2	-	
Turn-Off Delay Time		td(off)	-	38.2	-	
Fall Time		tf	-	12	-	

SOURCE–DRAIN DIODE CHARACTERISTICS

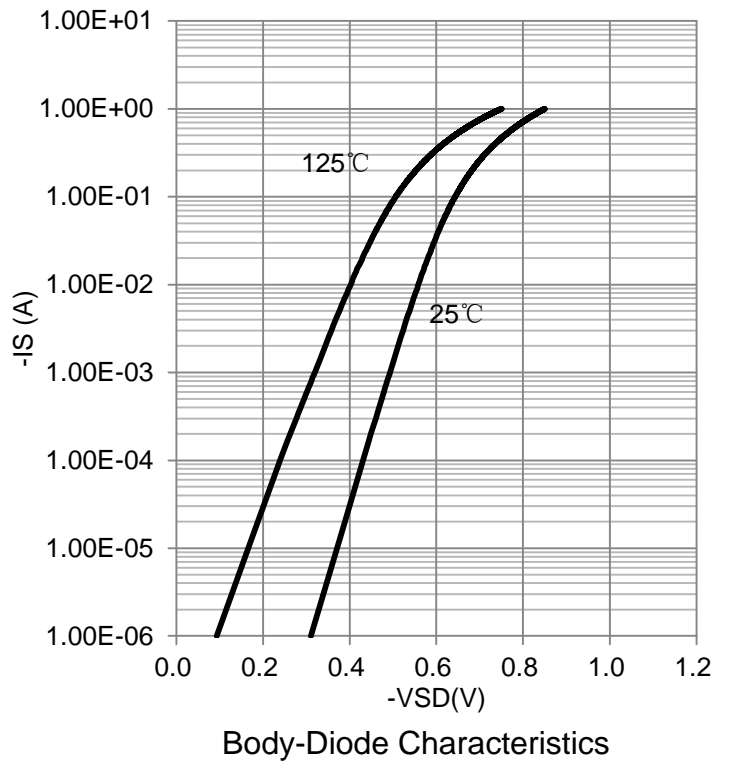
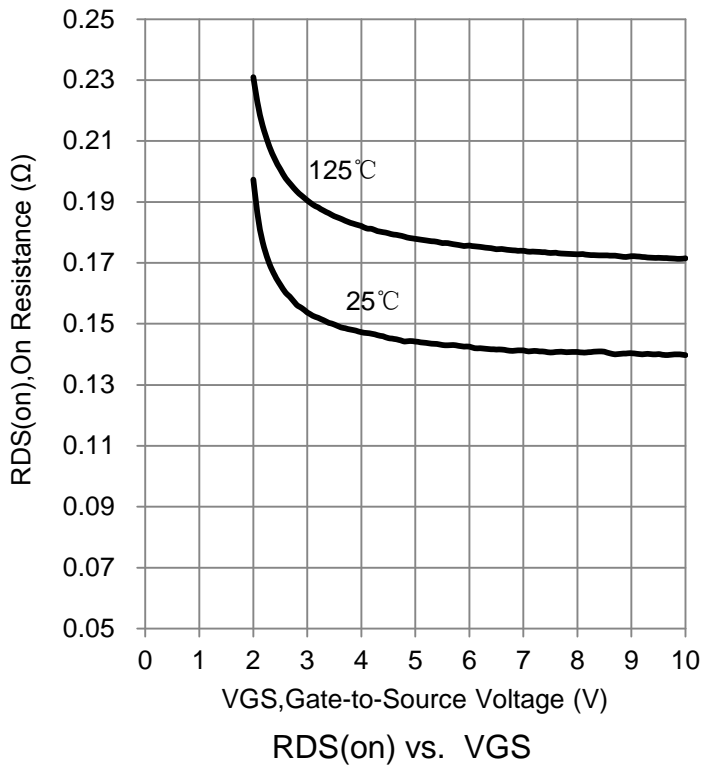
Forward Voltage (VGS = 0 Vdc, ISD = -1 Adc)	VSD	-	-0.75	-1	V
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3.Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

7. ELECTRICAL CHARACTERISTICS CURVES



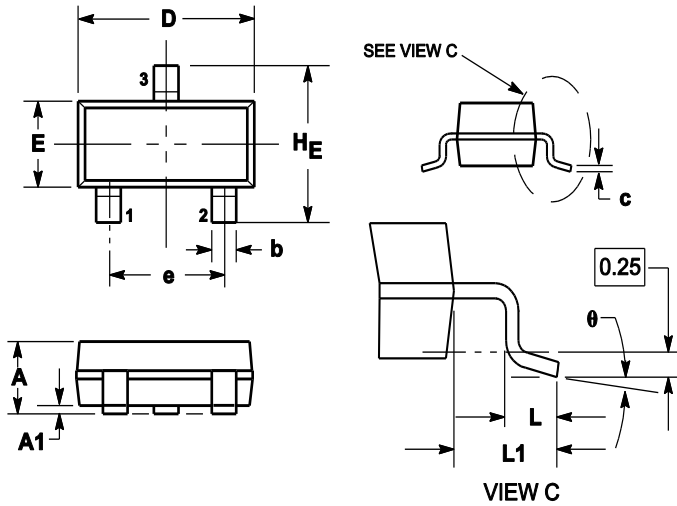
7. ELECTRICAL CHARACTERISTICS CURVES(Con.)



8. OUTLINE AND DIMENSIONS

Notes:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.



DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1	1.11	0.035	0.04	0.044
A1	0.01	0.06	0.1	0.001	0.002	0.004
b	0.37	0.44	0.5	0.015	0.018	0.02
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.9	3.04	0.11	0.114	0.12
E	1.20	1.3	1.4	0.047	0.051	0.055
e	1.78	1.9	2.04	0.07	0.075	0.081
L	0.10	0.2	0.3	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.4	2.64	0.083	0.094	0.104
θ	0°	---	10°	0°	---	10°

9. SOLDERING FOOTPRINT

